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SuperSpeed Device Design By Example, by John Hyde, is the latest in a series of "How-To" USB books. This guide takes a practical approach to designing and implementing SuperSpeed USB peripherals. Starting with the essential theory on USB 3.0, it provides a series of worked examples using the FX3 SuperSpeed Explorer Kit.

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SuperSpeed Explorer Kit The CYUSB3ACC-007 CPLD Accessory Board is designed to work with the GPIF II of the EZ-USB® FX3™ SuperSpeed Explorer Kit (CYUSB3KIT-003). It is meant to be used for the examples mentioned in the book SuperSpeed Device Design By Example, by John Hyde.

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One of the FX3 examples is a CPLD_Programmer so output from the Xilinx toolchain can be directly programmed into the CPLD; no additional hardware is required. The book describes step-by-step what you need to do to be successful with a SuperSpeed Device Design - after reading the book you will discover that it's not that difficult after all!

SuperSpeed Device Design By Example - By John Hyde

SuperSpeed Device Design By Example [John Hyde] on Amazon.com. *FREE* shipping on qualifying offers. This is a How-To book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols. The book is based on the Cypress FX3 SuperSpeed Device and the firmware examples ...

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Accelerate Your Design. Refer to the SuperSpeed Device Design By Example book, by John Hyde. This guide is optimized to work with the FX3 SuperSpeed Explorer kit. Work through a series of examples designed to teach you how to design a real-world USB 3.0 application.

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Code 51: This device is currently waiting on another device or set of devices to start. Code 44: An application or service has shut down this hardware. Solution: Refresh the computer and if problem still exist then restart the computer after uninstallation of device driver is complete.